




## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18  
Stylesheet Version v18.0

Title of Invention	DUAL STAGE VOLTAGE REGULATION CIRCUIT																																																																										
<p>Application Number: 10/666324 </p> <p>Confirmation Number: 7924</p> <p>First Named Applicant: Nicola Telecco</p> <p>Attorney Docket Number: ATM-228</p> <p>Art Unit: 2838</p> <p>Search string: ( 4835455 or 5831845 or 6114845 or 6188212 or 6194887 or 6362612 or 6369552 ).pn.</p> <p><b>US Patent Documents</b></p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td><input checked="" type="checkbox"/></td><td>1</td><td>4835455</td><td>1989-05-30</td><td>Coddington et al.</td><td>A1</td><td>323</td><td>314</td></tr><tr><td><input checked="" type="checkbox"/></td><td>2</td><td>5831845</td><td>1998-11-03</td><td>Zhou et al.</td><td>A1</td><td>363</td><td>60</td></tr><tr><td><input checked="" type="checkbox"/></td><td>3</td><td>6114845</td><td>2000-09-05</td><td>Capici et al.</td><td>A1</td><td>323</td><td>313</td></tr><tr><td><input checked="" type="checkbox"/></td><td>4</td><td>6188212</td><td>2001-02-13</td><td>Larson et al.</td><td>A1</td><td>323</td><td>281</td></tr><tr><td><input checked="" type="checkbox"/></td><td>5</td><td>6194887</td><td>2001-02-27</td><td>Tsukada</td><td>A1</td><td>323</td><td>315</td></tr><tr><td><input checked="" type="checkbox"/></td><td>6</td><td>6362612</td><td>2002-03-26</td><td>Harris</td><td>A1</td><td>323</td><td>312</td></tr><tr><td><input checked="" type="checkbox"/></td><td>7</td><td>6369552</td><td>2002-04-09</td><td>Goyhenetche et al.</td><td>A1</td><td>323</td><td>266</td></tr></tbody></table> <p><b>Signature</b></p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td><i>Gary L. Laxton</i></td><td>12-10-04</td></tr></tbody></table>								init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	<input checked="" type="checkbox"/>	1	4835455	1989-05-30	Coddington et al.	A1	323	314	<input checked="" type="checkbox"/>	2	5831845	1998-11-03	Zhou et al.	A1	363	60	<input checked="" type="checkbox"/>	3	6114845	2000-09-05	Capici et al.	A1	323	313	<input checked="" type="checkbox"/>	4	6188212	2001-02-13	Larson et al.	A1	323	281	<input checked="" type="checkbox"/>	5	6194887	2001-02-27	Tsukada	A1	323	315	<input checked="" type="checkbox"/>	6	6362612	2002-03-26	Harris	A1	323	312	<input checked="" type="checkbox"/>	7	6369552	2002-04-09	Goyhenetche et al.	A1	323	266	Examiner Name	Date	<i>Gary L. Laxton</i>	12-10-04
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FORM PTO-1449				Atty. Docket No. ATM-228	Serial No. 10/666,324	
LIST OF PRIOR ART CITED BY APPLICANT		Applicant: Nicola Telecco				
		Filing Date: Sept. 17, 2003		Group: 2838		
U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Grant Date	Name	Class	Sub Class	Filing Date
AA						
AB						
AC						
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FOREIGN PATENT DOCUMENTS						
Examiner Initial*	Document Number	Grant Date	Country	Class	Sub Class	Translation Yes No
AK						
AL						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
	AM	Book entitled "Flash Memories" by Paolo Cappelletti et al., pp. 332-335. <i>No Date</i>				
	AN					
	AO					
EXAMINER: <i>Gary L. Laxton</i>				DATE CONSIDERED: <i>12-10-04</i>		
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